



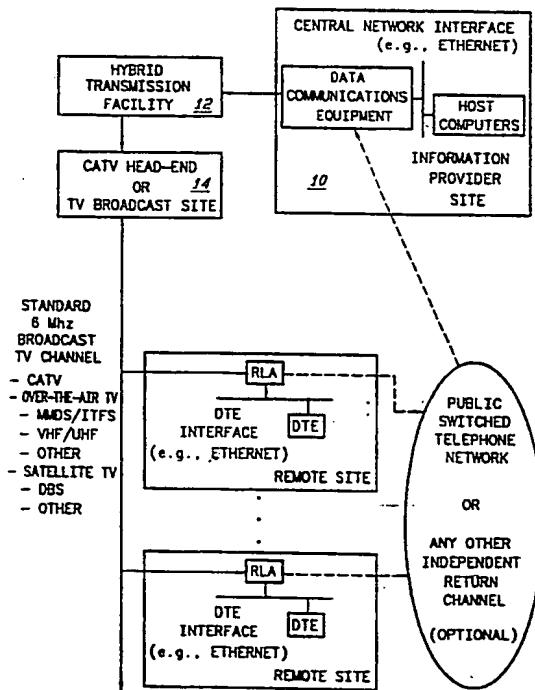
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(54) Title: REMOTE LINK ADAPTER FOR USE IN TV BROADCAST DATA TRANSMISSION SYSTEM

(57) Abstract

A hybrid transmission system (10, 12 and 14) is provided to transmit and receive high-speed digital information in the form of variable length packets using standard television practices and components (14). The basic building block of this hybrid digital transmission system is the device (RLA) at the remote location that receives the analog broadcast TV-like signal processed by a standard vestigial sideband video modulator. This device decodes the digital information from the signal and then passes it along as digital information to any form of a data terminal equipment (DTE) or computer.



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REMOTE LINK ADAPTER FOR USE IN TV BROADCAST
DATA TRANSMISSION SYSTEM

BACKGROUND OF THE INVENTION

5 This invention relates generally to data transmission by television (TV) broadcast, and more particularly the invention relates to a remote link adapter for use in receiving broadcast data.

10 At the present time there is no cost effective method of delivering at high-speed packets of digital information to remote locations like homes and mobile vehicles. As used herein, digital information are packets that may contain multimedia data (sound, pictures, text, graphics, video) or executable computer code in addition to addressing status and protocol data. The existing 15 telephone circuits, cellular systems and radio systems are just too slow to be practical and useful in an ever increasing number of digital multimedia applications including graphics, imaging, animation or remote windows. High-speed leased telephone lines and/or high-speed 20 broadband switched digital service, offered by the telephone companies, are too expensive or not yet available to any average user (consumer) or potential user and do not address the needs of mobile users. In addition, all other existing forms of digital transmission techniques to the 25 remote location (also known as the last mile) within any particular metropolitan area are too expensive and impractical to be useful at the present time. Most of these existing forms of high-speed digital transmission to the remote location are well known in the prior art but 30 none of them attempt to use a low-cost, high-speed hybrid transmission scheme as described herein.

35 There are some existing forms of encoding digital information into the vertical blanking intervals (VBI) of a standard NTSC baseband TV signal, but again, these techniques provide severe difficulties with synchronization

and the reception of digital information when it is delivered in the form of variable length data packets. These difficulties translate into higher costs at the receiving site, making those techniques also impractical 5 when used to broadcast high-speed (greater than 10 Mbps) digital information in the form of variable length packets (also referred to herein as "addressable broadcasting" digital services) at very low cost.

This present invention relates to a very cost effective method of delivering high-speed (e.g., 10 Mega bits per second, Mbps, or higher) digital information in the form of packets to any remote location such as a home, school, office or mobile vehicle using standard TV practices and components and as part of a very 10 comprehensive hybrid transmission system. More 15 particularly, the invention provides a device for receiving broadcast data, transmitting the data to the end user, and communicating as necessary with the information provider.

SUMMARY OF THE INVENTION

Accordingly, an object of the invention is a practical method of transmitting and receiving high speed 20 digital information at remote sites.

Another object of the invention is an adapter for receiving television broadcast data.

Briefly, in accordance with the invention, a 25 hybrid transmission system transmits and receives high-speed digital information in the form of variable length packets using standard television practices and components. The basic building block of this hybrid digital transmission system is the remote link adapter device at 30 the remote location that receives the analog broadcast TV-like signal processed by a standard vestigial sideband video modulator. The device decodes the digital information from the signal and then passes it along as

digital information to any form of a data terminal equipment or computer.

The remote link adapter includes a hybrid interface, a user interface, and a control means. The hybrid interface includes a radio modem for receiving the broadcast channel, and an optional return channel having a telephone modem, a radio link interface or a CATV interface for use in an on-demand addressable broadcast system. The user interface can include an Ethernet interface, a personal computer interface (e.g. PC/AT or MAC), or a direct bus into a video game system such as the Nintendo NES or Super NES. The control means includes a microprocessor, memory, and a stored control program.

The invention and objects and features thereof will be more readily apparent from the following detailed description and appended claims when taken with the drawings.

BRIEF DESCRIPTION OF THE DRAWING

FIGURE 1 is a functional block diagram of a data transmission system employing remote link adapters in accordance with the invention.

FIGURE 2 is a functional block diagram of a remote link adapter as used in the system of FIG. 1.

FIGURES 3-5 are functional block diagrams of three different embodiments of the remote link adapter illustrated in FIG. 2.

FIGURE 6 is a functional block diagram of the RF portion of the data transmission system of FIG. 1.

FIGURE 7A is a schematic of a data scrambler and FIGURE 7B is a schematic of a data descrambler-as used in FIG. 6.

FIGURE 8A is a schematic of a three level encoder, and FIGURE 8B is a schematic of a three level decoder as used in FIG. 6.

DETAILED DESCRIPTION OF THE ILLUSTRATIVE EMBODIMENTS

FIG. 1 is a functional block diagram of a data transmission system for broadcasting data to remote locations and employing remote link adapters in accordance with the invention. The system includes an Information Provider site 10, a Hybrid Transmission facility (HTF) 12, a CATV head-end or TV broadcast site 14 which transmits the data over a standard 6Mhz television channel, and the Remote sites which receive the incoming TV signal.

The central site data communications equipment (DCE) at the Information Provider's site sends a high-speed digital signal to the Hybrid Transmission Facility (HTF) via any suitable high-speed line or media. In the case where the Information Provider's site is co-located with the HTF, the connection between the central site DCE and the HTF can be a short piece of coaxial cable.

The HTF receives the incoming high-speed digital signal, combines it with other incoming high-speed digital signal (using local area network-LAN-based fast-packet switching techniques) to the appropriate outgoing port for broadcasting purposes. The signal is then transmitted to the appropriate head-end location or over-the-air TV broadcast site either as an analog signal or as a digital signal. Again, the head-end site or TV broadcast site could be co-located with the HTF.

Before the digital signal is broadcast over the air or transmitted downstream throughout a cable television (CATV) network, the digital signal is first encoded into a standard 1 volt peak-to-peak, baseband input into a standard video modulator. The signal is then processed by the video modulator just like it processes a standard 1 volt peak-to-peak baseband television input signal and is then transmitted over a standard 6 Mhz television channel.

The Remote Link Adapter (RLA) at the remote site receives the incoming TV-like analog signal using a built-in standard TV tuner/receiver. After processing the

signal, the RLA presents the resulting digital signal to the data terminal equipment (DTE) interface. When a return channel is present, the RLA also forwards digital information (packets) to the central site using the return channel. The return channel(s) can be a standard public switched network telephone line(s) operating at a different speed from the forward broadcast TV channel or any other available channel, such as a radio channel, or the CATV system itself.

Finally, if a return channel is present, the central site DCE will receive the incoming digital information (packets) from the return channel and forward them to the central network interface to complete the circuit. However, if a return channel is not present, the RLA simply selects digital information (packets) from the high-speed forward broadcast channel based on a specific selection criteria defined by software. A hybrid system as described herein without a return channel is also known as a "selectable addressable broadcast system". A hybrid system as described herein with a return channel is also known as a "on-demand addressable broadcast system". One of the unique characteristics of the hybrid system is that the forward broadcast channel is completely independent from any form of a return channel. This feature allows each channel to be optimized independently from the other.

FIGS. 2-5 are functional block diagrams of embodiments of the RLA. The RLA receives the high-speed analog signal from the 6 Mhz broadcast TV channel, processes the signal and then delivers the resulting digital signal to a specific DTE interface. Physically, the RLA will either be a small standalone box or a printed circuit card (PC board) that can be mounted inside any number of DTE units including computers, workstations, home computers, interactive graphics terminals or simple video game machines.

FIG. 2 illustrates the basic building blocks for the RLA. As illustrated, the overall RLA design consists of three basic parts: (1) The User Interface 20 which is also sometimes referred to as The DTE Interface, (2) The Hybrid Interface 22 which includes the forward broadcast channel and an optional return back channel, and (3) The control means or The Engine 24 which includes the microprocessor, memory, control and optional security portions of the RLA.

FIG. 3 illustrates the implementation of a fully configured RLA. In this implementation, the user interface 20 is Ethernet, the hybrid interface 22 is a high-speed (approximately 10 Mbps) RF modem used to receive the forward broadcast channel and a built-in 9600 bps telephone modem used to transmit the return channel. The engine 24 of the RLA is a standard Intel microprocessor with associated read only memory (ROM), random access memory (RAM) and a digital encryption standard (DES) co-processor chip-set used to decipher the incoming digital information from the broadcast channel. This RLA device is a small standalone box with an Ethernet interface, an RF "F" connector interface to receive the high-speed broadcast channel, and an RJ-11 telephone jack used for the return channel.

FIG. 4 illustrates the implementation of the RLA for use with a personal computer bus. In this case, the user interface 20 is a direct computer bus interface. The hybrid interface 22 of this embodiment is identical to the implementation of FIG. 2 but the engine 24 is a less capable microprocessor with a smaller amount of ROM and RAM and no encryption co-processor.

This RLA device is a PC board form factor product that interfaces directly with either a PC-AT bus (model 200-001), a MacII bus (model 200-002) or a PS/2 bus (model 200-003). The PC board also provides an RF "F" connector

to receive the high-speed broadcast signal and an RJ-11 telephone jack for the return channel.

FIG. 5 illustrates the implementation of the RLA for use with a video game machine such as the Nintendo NES or Super NES. This implementation is the lowest cost implementation of the RLA. The user interface 20 is a direct bus interface into the NES or Super NES. In this implementation, the memory and memory control portions of the RLA are implemented in the form of a standard NES or Super NES video game cartridge. The memory control function 24 uses a single VLSI custom chip. The hybrid interface is a very low-cost high-speed (approximately 10 Mbps) RF modem, with no return channel and the engine is another single VLSI custom integrated circuit (IC). Finally, an optional expansion interface is also provided in case a future return channel upgrade is required. The same expansion port may also allow for the attachment of an external printer or an external bulk storage device or other peripherals. This RLA device is packaged in a small plastic enclosure that connects directly to a Nintendo NES or Super NES video game machine. The device provides an RF "F" connector to receive the high-speed broadcast channel and an optional expansion port. A return channel will normally not be available in this RLA implementation.

The RLAs described in accordance with this invention are designed to operate in a hybrid transmission system with a specific forward broadcast channel ("IN Channel") protocol. The IN Channel protocol specifies the rules used by the RLAs to receive and filter the relevant digital information (packets) flowing in the forward broadcast channel. Each RLA has a unique digitally encoded address. RLAs are capable of receiving broadcast packets, multicast packets (packets addressed to groups of users/RLAs) and packets specifically addressed to them. The forward broadcast channel (IN Channel) is a shared channel. Many RLAs may share the same channel to receive

5 digital information. When an RLA is powered up and the RF modem is appropriately tuned to a particular IN Channel (6 Mhz broadcast TV Channel), the RLA modem automatically synchronizes to the incoming high-speed data being broadcast and starts monitoring (receiving) the incoming signal. The RLA firmware, after its initial power-up and self test sequence, instructs the RLA to look for a particular address or set of addresses (e.g., broadcast packets). Once packets with these addresses are detected, 10 the RLA then forwards the appropriate incoming packets to the DTE for further processing.

15 Selectable Broadcast -- All RLA units receive the main-menu packet from the channel. Then, based on user selections, the RLA units filter the appropriate broadcast packets from the channel in the next broadcasts. Broadcasts are repeated periodically. Based upon the nature of the digital data, certain packets are re-broadcast approximately every other second while other packets are re-broadcast approximately every minute. The 20 information contained in a particular broadcast cycle (sequence of packets) will also change with time.

25 On-Demand Broadcast -- Users request a particular set of information (packets) in accordance with any given data communications protocol supported by The IN Channel. The RLA will support most standard and defacto protocols that run over a standard IEEE-802.3 Ethernet local area network. The following are the different types of on-demand calls that can be set-up using The IN Channel:

30 a. Broadcast/Multicast -- The Information Provider broadcasts or multicasts the information to the particular user or group of users. No call set-up is required. The request for information is completely asynchronous from the delivery of information;

5

- b. Secure Sessions -- This is the most complex procedure since it involves the distribution of encryption keys;
- c. Non-secure sessions -- This procedure is identical to the procedures currently used by existing network applications. The IN Channel is transparent to most LAN-based network applications;

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- d. Interactive sessions for the Consumers -- These procedures are determined by the different Information Providers, who decide to use the IN Channel to deliver their services.

15 For on-demand applications -- The RLA shown in FIG. 3 goes through an initial call set-up/security procedure before it becomes operational. First, the user dials a particular telephone number. Dial-back security and user authentication will be optional at this stage. Once the central site DCE (located at the Information Provider's site) answers the call, the RLA goes through a security protocol (encryption key distribution) if used in the system. Next a link layer connection is established between the central site DCE and the remote RLA. This connection remains transparent to the applications running above. The hybrid connection looks like a transparent remote Ethernet bridge and, therefore, is compatible with all upper layer protocols (e.g., TCP/IP, AppleTalk, ISO, DECNET, etc.) that can run over Ethernet. Once the connection is established, the remote user can now run standard network applications just as if the user were located at the central site local area network. The hybrid connection is terminated by hanging up the telephone side of the connection (the return channel).

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35 The RLA shown in FIG. 4 series also goes through an initial call set-up procedure before it becomes

operational. Again, first, the user dials a particular telephone number. Dial back security and user authentication will be optional at this stage. Once the central site DCE answers the call, a link layer connection
5 is established between the central site DCE and the remote RLA. Since the remote RLA is a PC board that fits inside a particular personal computer, specific network driver software is required by the RLA. Network applications need to be ported to the hybrid network driver in order to be
10 able to run over the hybrid connection.

The RLA shown in FIG. 5 normally does not go through a call set-up procedure since no return channel is used. This embodiment operates in accordance with the IN Channel protocol.
15

The IN Channel uses a protocol that controls the flow of information and the addressing in the channel. All RLAs will have a unique address. In addition, the RLAs will also be able to receive broadcast and multicast messages (packets). The following is a list of the
20 different types of packets that can flow in the IN Channel:

1. Broadcast packets (including selectable broadcast packets)
2. Multicast packets
3. On-Demand Addressable packets
 - Secure Packets
 - Non-secure Packets
 - Network Management Packets
4. Addressable video frame packets (followed by one or more NTSC video frames)

25 The channel transports variable length data packets. Packet multiplexing techniques are primarily based on a FIFO (first in first out) scheme as a direct feed from a LAN Switch port. A priority scheme is given to certain packets, including selectable broadcasts and some
30

network management packets. The IN Channel operates in a synchronous mode. Both on-demand and selectable broadcast traffic is transported in the IN Channel at the same time. Bandwidth management is performed by the equipment at the
5 Hybrid Regional Distribution Centers.

The digital information (packets) carried by The IN Channel are transparent to the applications. Standard upper-layer protocols such as: TCP/IP, NFS, X-Windows, AppleTalk, Netware and ISO will be able to run unchanged
10 over the IN Channel. However, upper-layer protocol optimization and/or new upper-layer protocols may be used for consumer-based applications.

The information flowing in The IN Channel is packaged in packets. Each packet contains the address of
15 the subscriber (destination address) and the source address (optional). LAN-based fast packet switching and multiplexing techniques will be used to process and transport information in the channel. Off-the-shelf packet switching equipment can be used at the regional
20 distribution centers.

The hybrid data transmission system return channel is optional. It needs to be used, however, when running real-time interactive applications. The return channel protocol provides data compression needed to
25 improve the performance of high-speed interactive applications.

The packet-data delivery via The IN Channel has a specific error rate. The IN Channel is a datagram, packet-based transport system. This system relies on the bandwidth and services provided by the CATV (cable or wireless) operators. Error recovery is achieved by the end-to-end transport layer protocol running in the data terminal equipment (DTE). A good example here is TCP/IP.
30 For selectable traffic, packets with CRC errors will be dropped. They will be received by the RLA on the next re-broadcast.

FIG. 6 shows the signal path from the head end cable TV transmitter site to a typical terminal. The system translates between NRZ baseband digital stream at the input to a modulation form suitable for use on a standard CATV head end modulator, and translates the received signals from a standard TV tuner connected to a cable TV network to baseband NRZ data stream and a regenerated clock. A TV tuner containing only those features required for receiving the digitally modulated CATV signal is used. The design is adaptable to a range of bit rates.

The data source 50 can be any baseband NRZ data stream. The clock 52 can be part of the system or supplied by the data source. The scrambler 54 takes a data stream supplied by the user and exclusively ORs the stream with a pseudorandom pattern which is a function of the past data. This breaks up periodic bit patterns which would degrade the bit synchronizer functioning.

The 3 level encoder 56 takes in a NRZ bit stream and clock and converts it to a more bandwidth efficient class IV partial response 3 level format. The output is not bandwidth limited because the CATV modulator contains SAW filters that produce the bandwidth limiting without any added phase distortion. A format that does not contain spectral terms at DC is used so that the CATV modulator and tuner not having a frequency response down to DC will not be a hindrance. The TV transmitter 58 is a standard unit commonly used in CATV or over the air TV systems. It takes a baseband video signal and converts it to any commonly used TV format.

The TV distribution network 60 can be any distribution system used to distribute TV signals. It can be electrical cable, optical cable, or over the air radio or microwave or direct broadcast satellite. The television signal containing the data is converted to baseband by a TV tuner 62 with only those portions necessary to receive the

modulated signal from the TV distribution network and produce a baseband video output consisting of the 3 level baseband signal. The 3 level decoder 64 takes the 3 level signal from the video output of the TV tuner 62 and converts it into two level NRZ data using a clock produced by the bit synchronizer 66. The bit synchronizer 62 regenerates the data clock from the video output of the TV tuner. The descrambler 68 takes in the data stream from the scrambler (after being modulated, transmitted, received, and demodulated by the RF components) and recovers the original data stream.

FIG. 7A is a schematic of one embodiment of the scrambler. The scrambler takes input data and XORs the data with a 23 bit PRNG sequence to make it more random. The circuit is designed as a two port network. Clock and data go to the input, and clock and scrambled data come from the output which matches the inputs on the 3 level encoder. The circuit performs an additional function as a test pattern generator. The pull up resistor on the data input provides all ones which produces a PRNG output.

The descrambler shown schematically in FIG. 7B performs the inverse function of the scrambler. It is designed as a two port network with clock and scrambled data going in the input, and clock and data coming from the output.

FIG. 8A is a schematic of the three level encoder. It uses class 4 duobinary encoding format created by subtracting a 2 bit delayed version of the input from itself. A zero input produces a middle level output and a one input produces one of the extreme levels. The second and subsequent one in a sequence keeps the output at the same level as caused by the first one.

FIG. 8B is a schematic of the three level decoder. It uses comparators to detect the three levels. The voltage levels for comparison are set by a combination of two items. The first is a factory set trimpot. Small

adjustments around this value are set by a feedback network that tries to make the two extreme voltage level outputs (ones) occur 25% of the time. This is the situation that exists with random data such as produced by scrambled data.

5 The data is sampled at the proper time by a locally regenerated clock.

There has been described a Remote Link Adapter used to receive high speed data transmitted by television broadcast. The RLA can be employed in interfacing with a LAN, an individual personal computer, or a video game machine. While the invention has been described with reference to specific embodiments, the description is illustrative of the invention and is not to be construed as limiting the invention. Various modifications and applications may occur to those skilled in the art without departing from the true spirit and scope of the invention as defined by the appended claims.

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WHAT IS CLAIMED IS:

1. In a high speed digital information transmission system in which digital data is broadcast using television components to a remote site, a remote link adapter comprising a hybrid interface for receiving and
5 demodulating a received television-like signal to obtain the transmitted digital data, a user interface for providing said digital data to data terminal equipment, microprocessor control means for controlling said hybrid interface in accordance with a protocol for controlling the
10 flow of said digital data and the addressing in the transmission channel, and a bus interconnecting said user interface, said hybrid interface, and said microprocessor control means.

2. The remote link adapter as defined by claim
15 1 wherein said hybrid interface further includes a return channel to an information provider wherein said system operates in an on-demand addressable broadcast mode.

3. The remote link adapter as defined by claim
20 2 wherein said return channel includes a telephone line modem.

4. The remote link adapter as defined by claim
2 wherein said return channel includes a radio interface.

5. The remote link adapter as defined by claim
2 wherein said return channel includes a CATV interface.

25 6. The remote link adapter as defined by claim
2 wherein said user interface includes an Ethernet interface.

7. The remote link adapter as defined by claim
2 wherein said user interface includes a personal computer
bus interface.

5 8. The remote link adapter as defined by claim
1 wherein said system operates in a selectable addressable
broadcast mode.

9. The remote link adapter as defined by claim
8 wherein said user interface interfaces with a video game
system.

10 10. The remote link adapter as defined by claim
1 wherein said hybrid interface includes an RF modem.

15 11. The remote link adapter as defined by claim
1 wherein said microprocessor control means controls the
addressing of said hybrid interface and the flow of data in
said bus.

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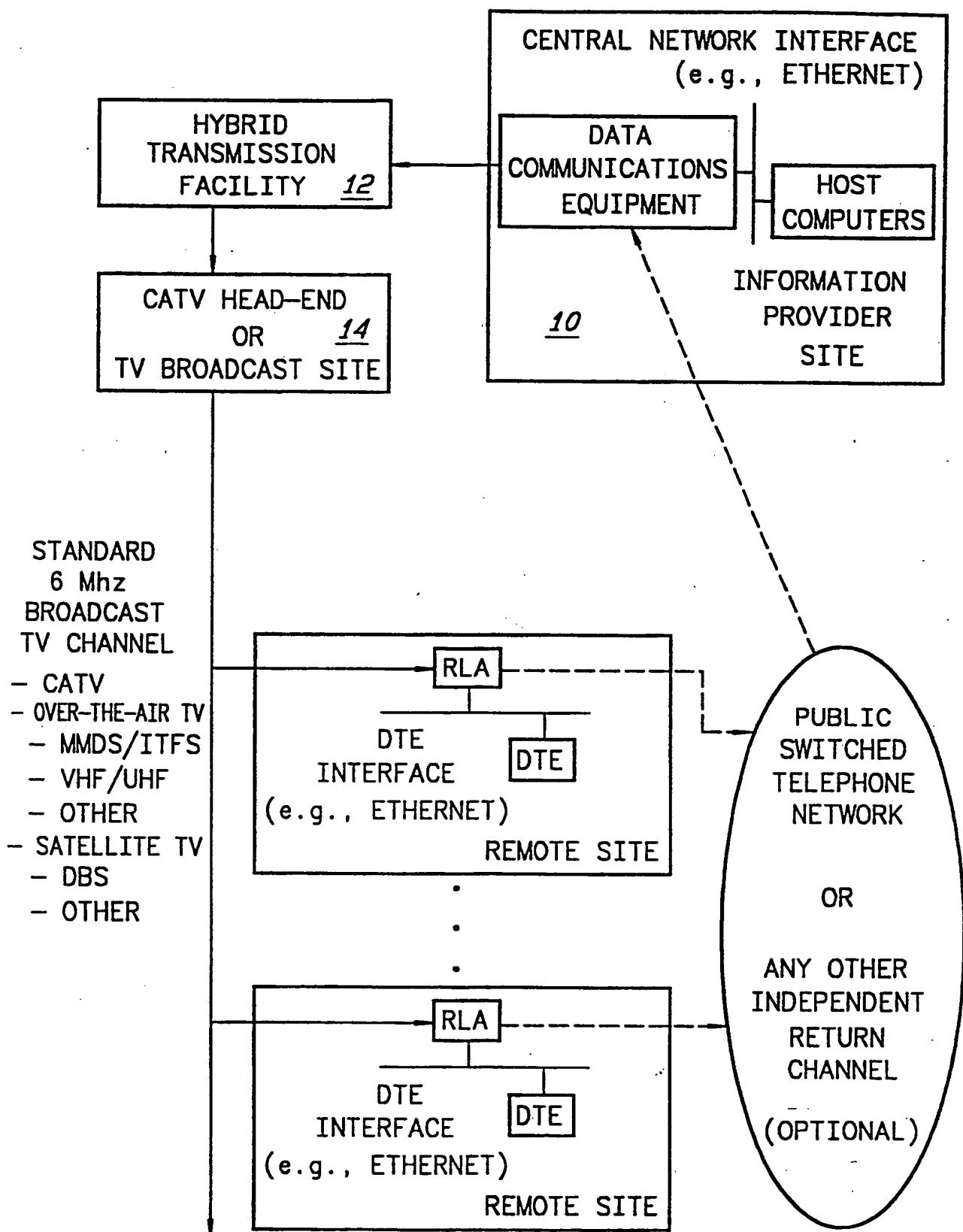


FIG. 1

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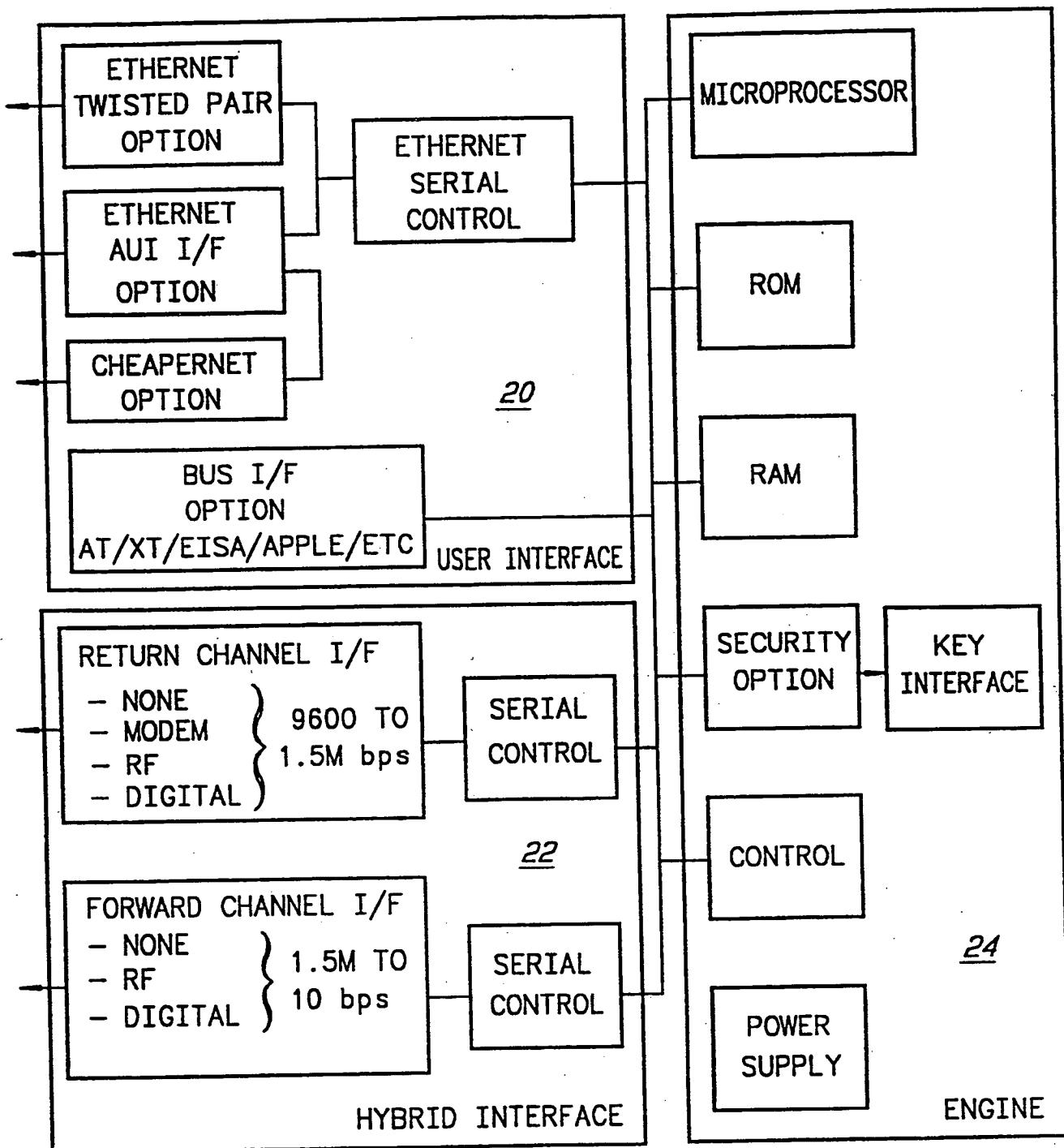


FIG. 2

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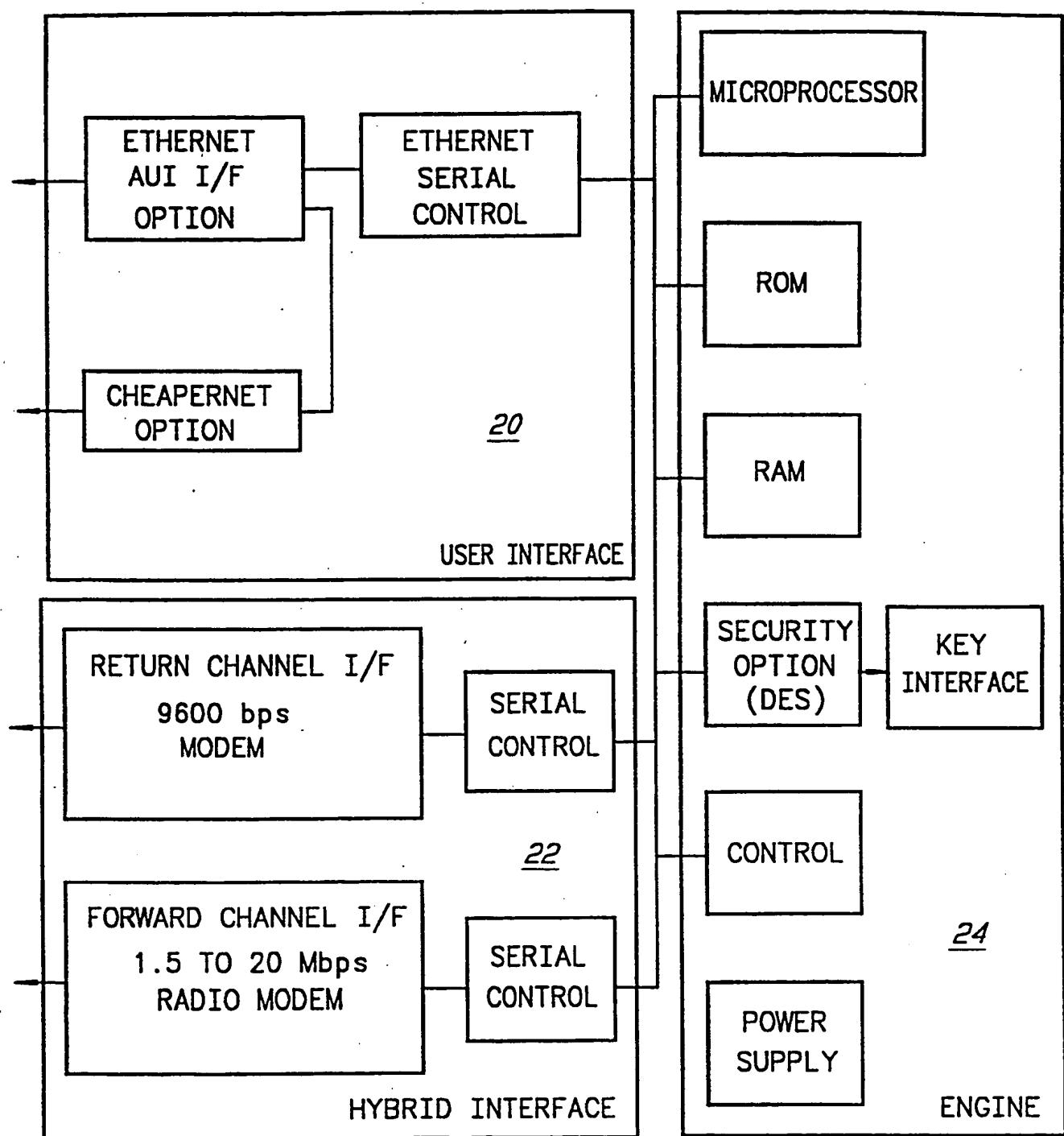


FIG. 3

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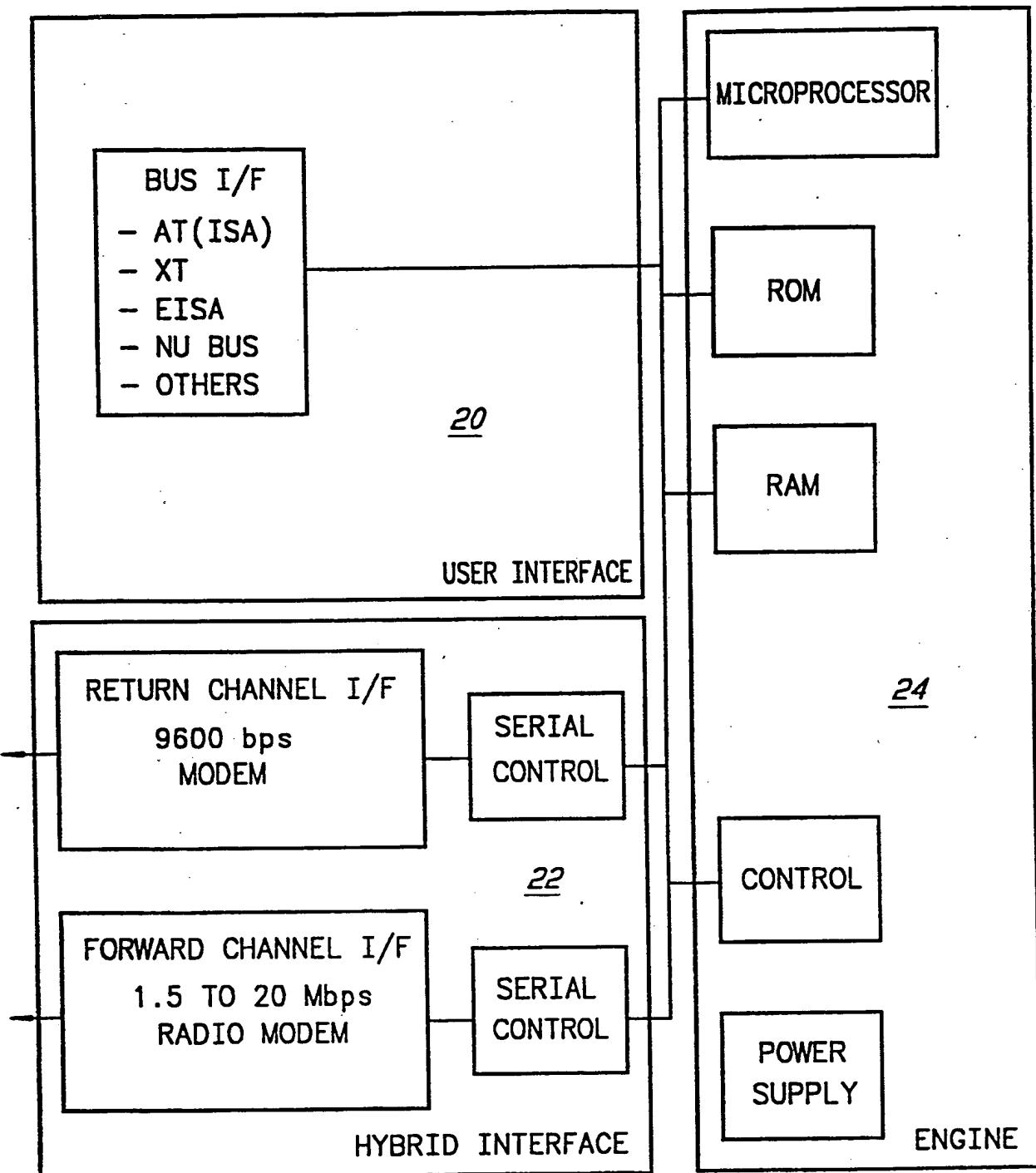


FIG. 4

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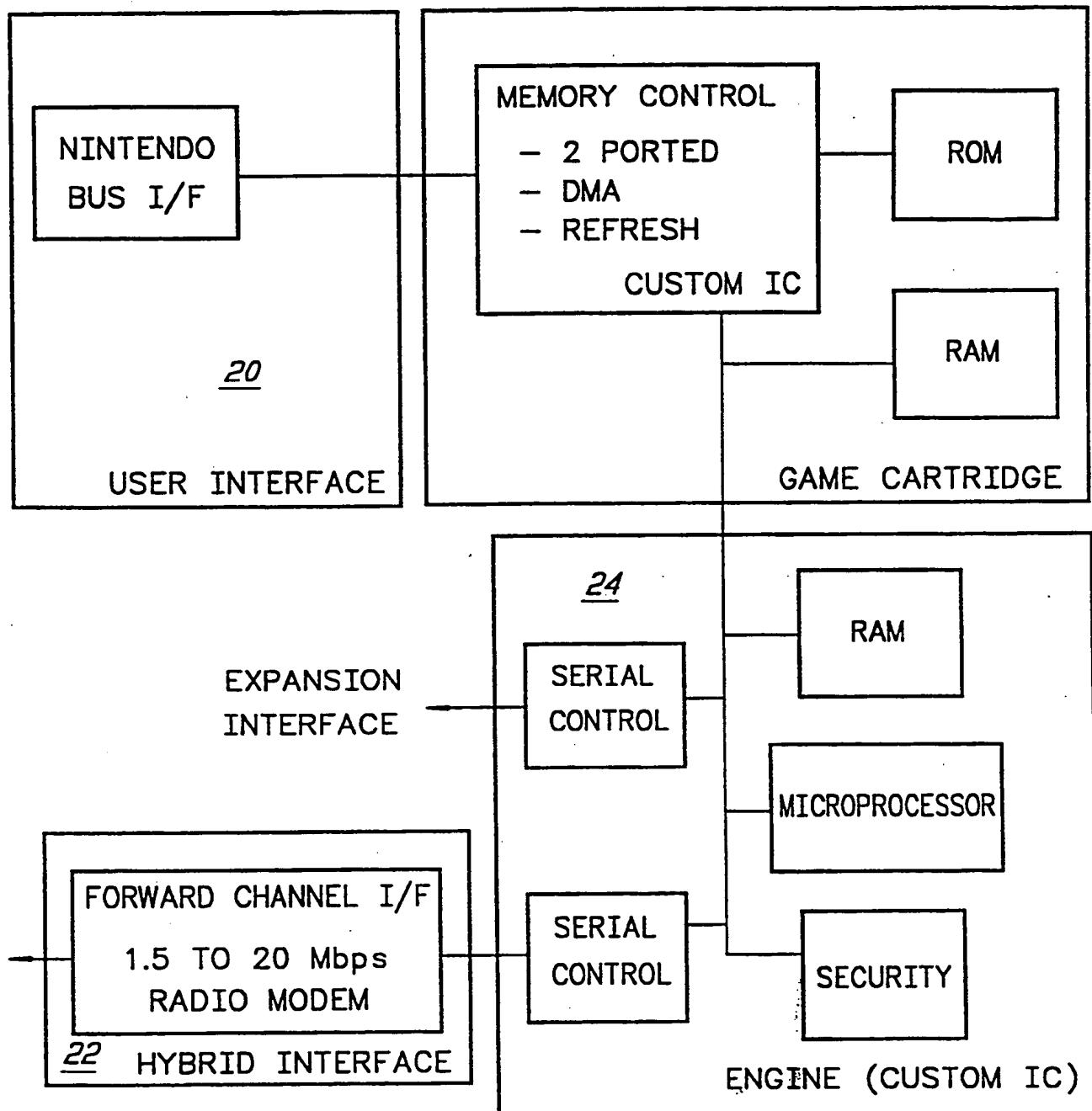


FIG. 5

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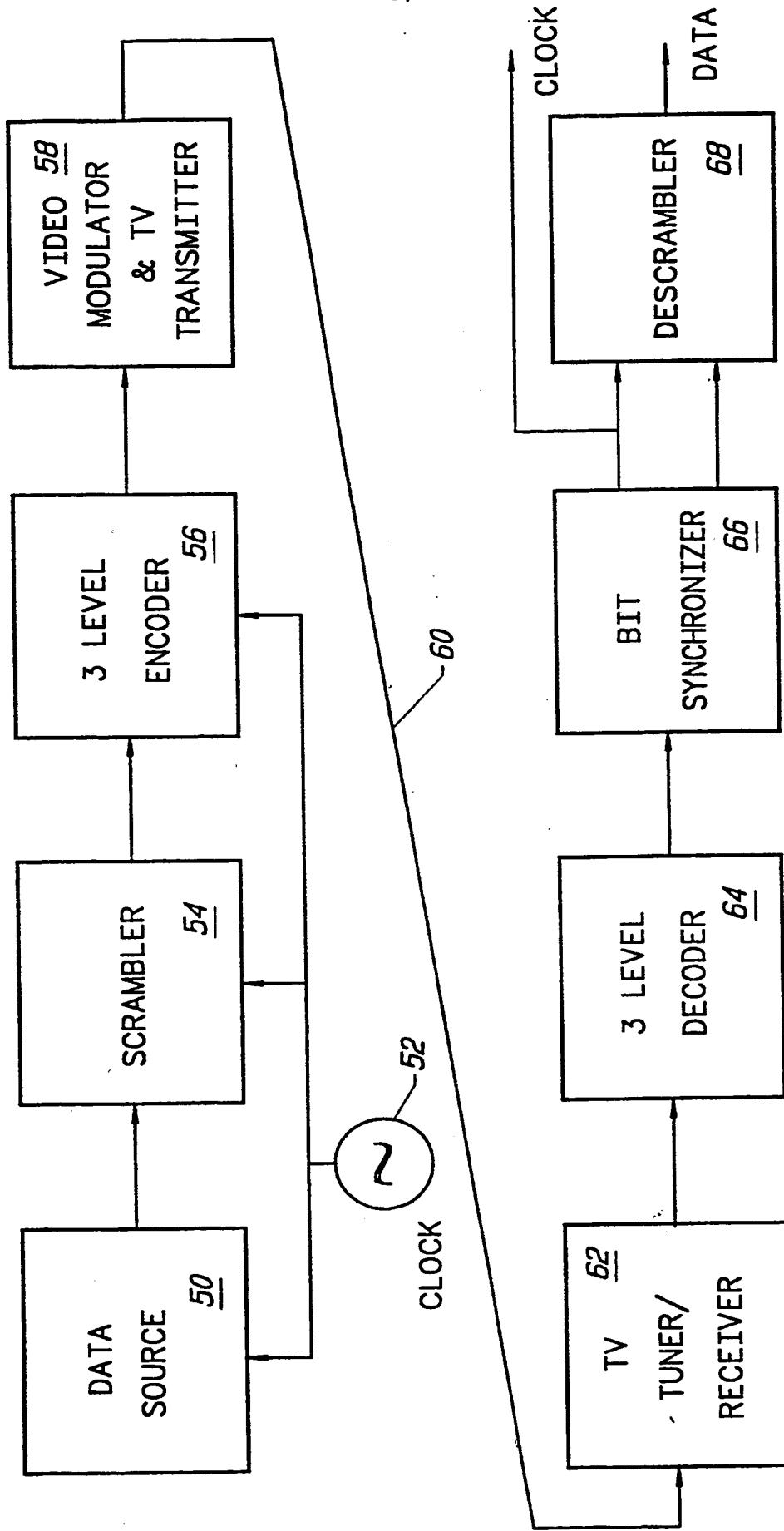


FIG. 6

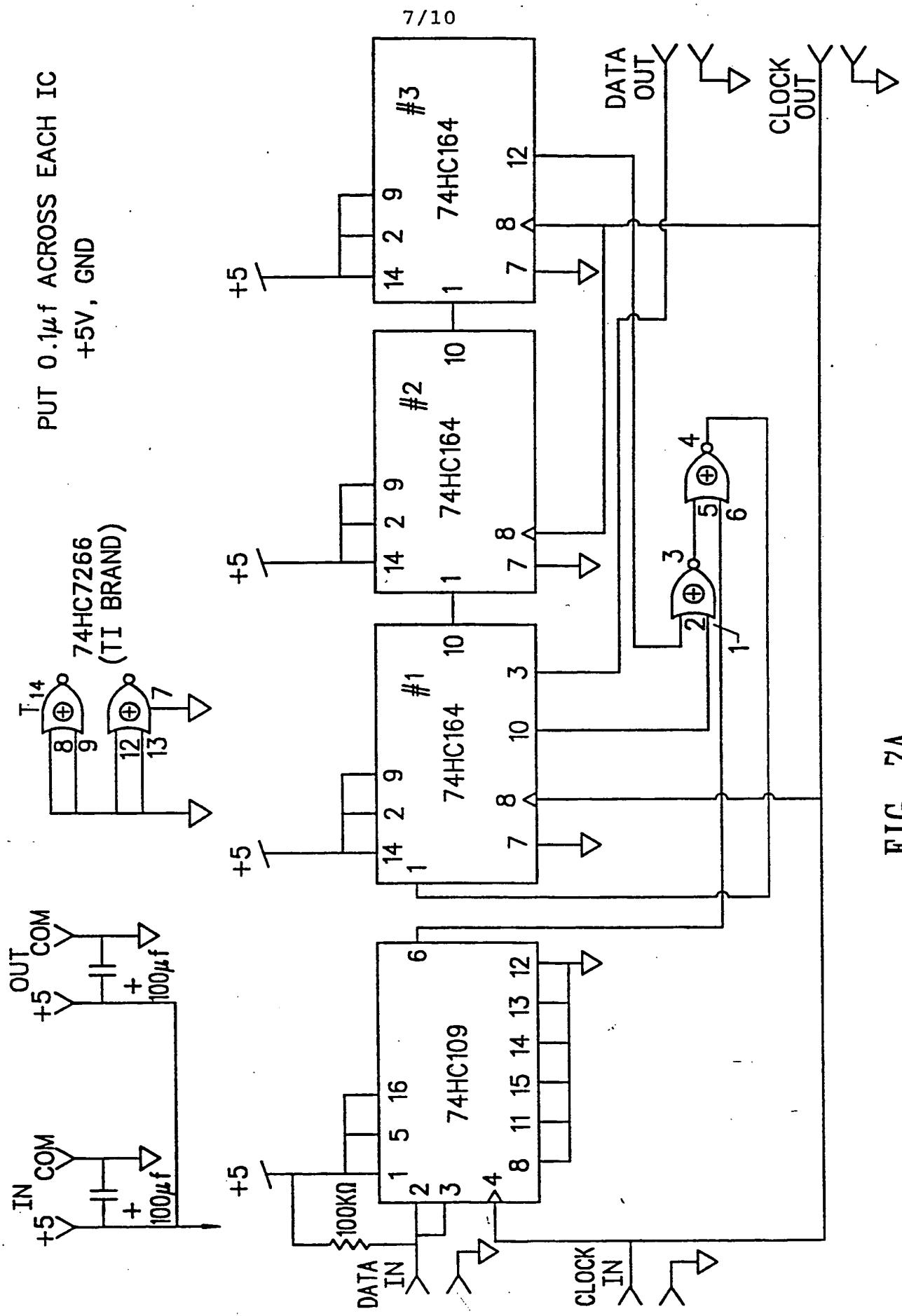


FIG. 7A

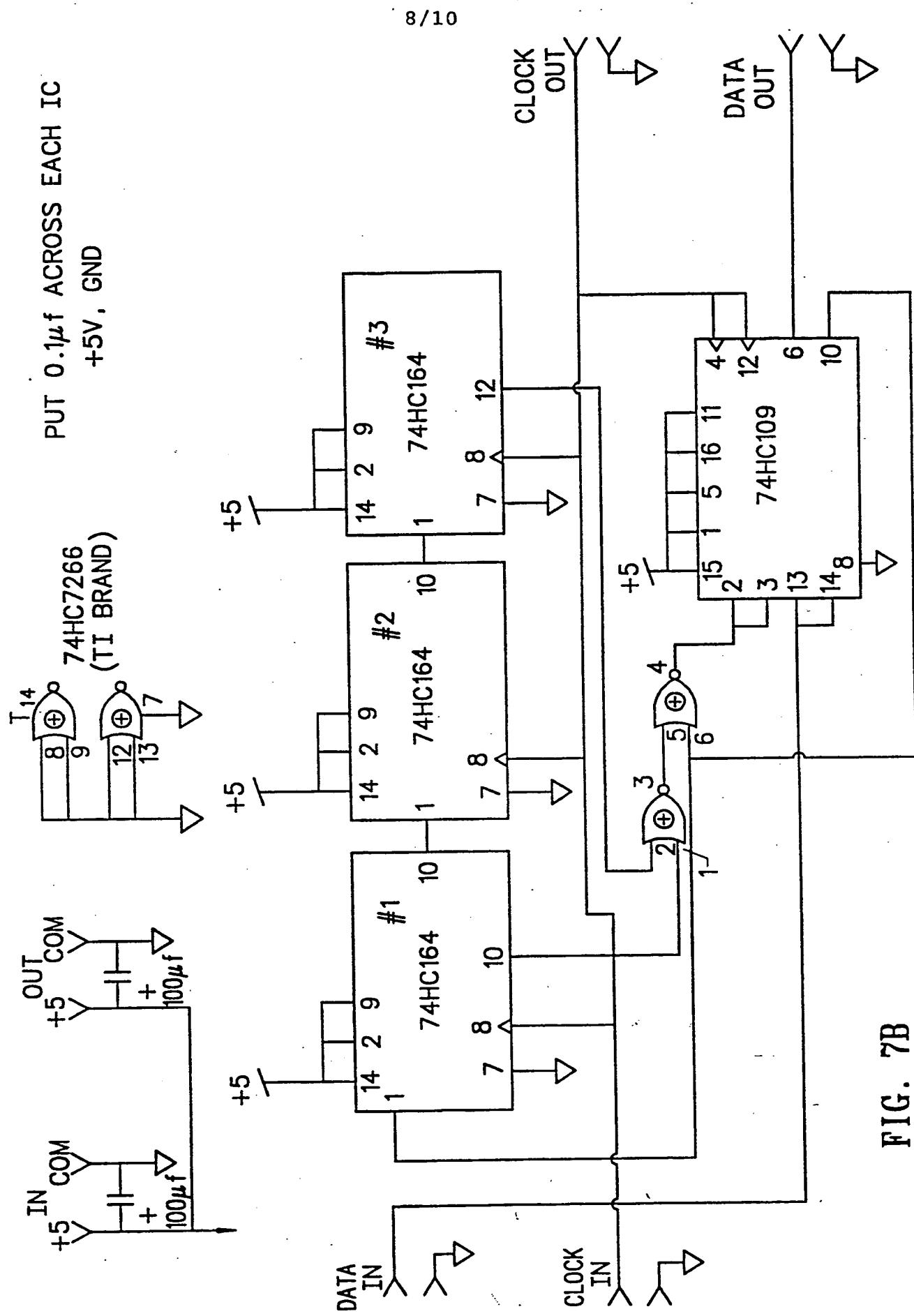


FIG. 7B

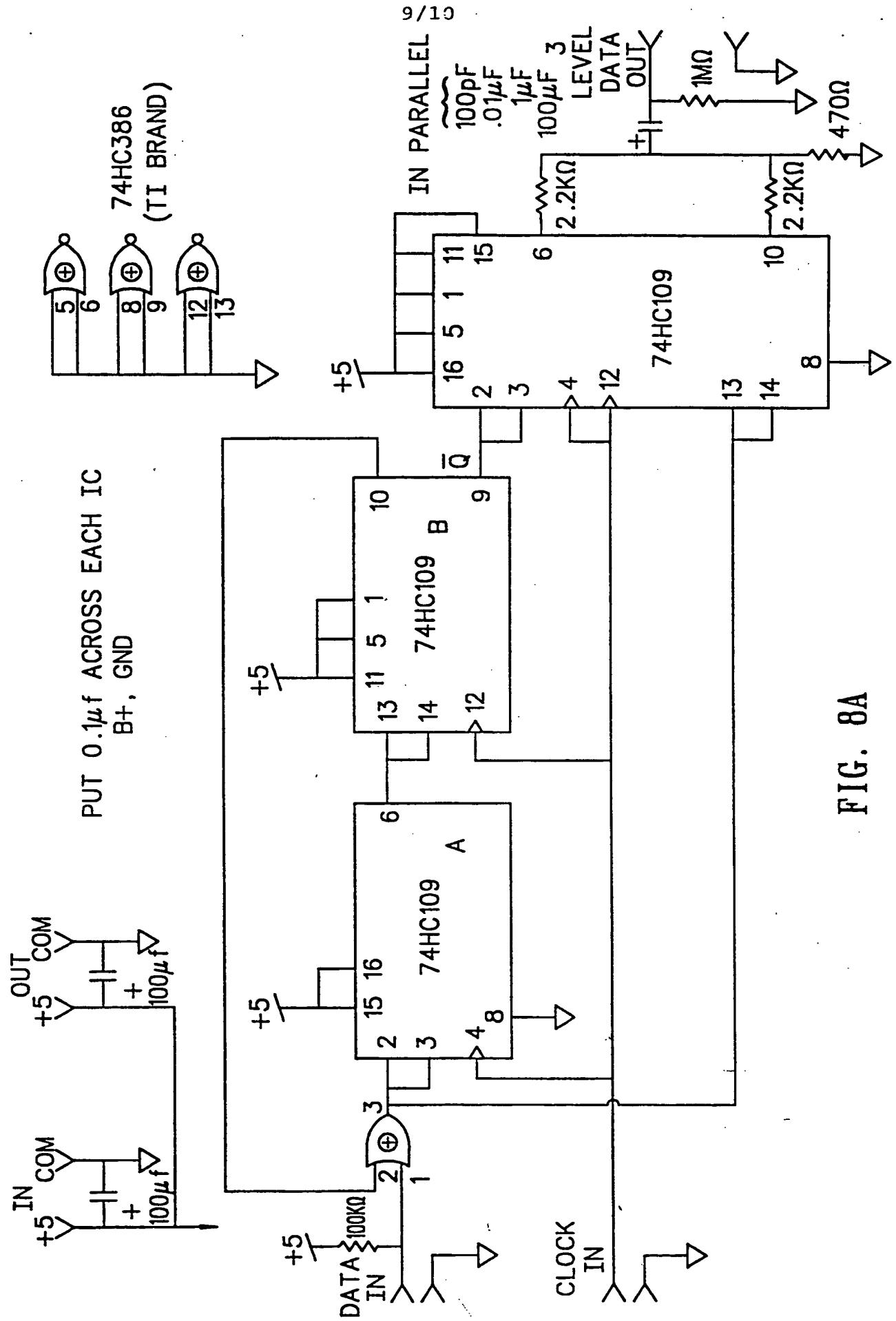
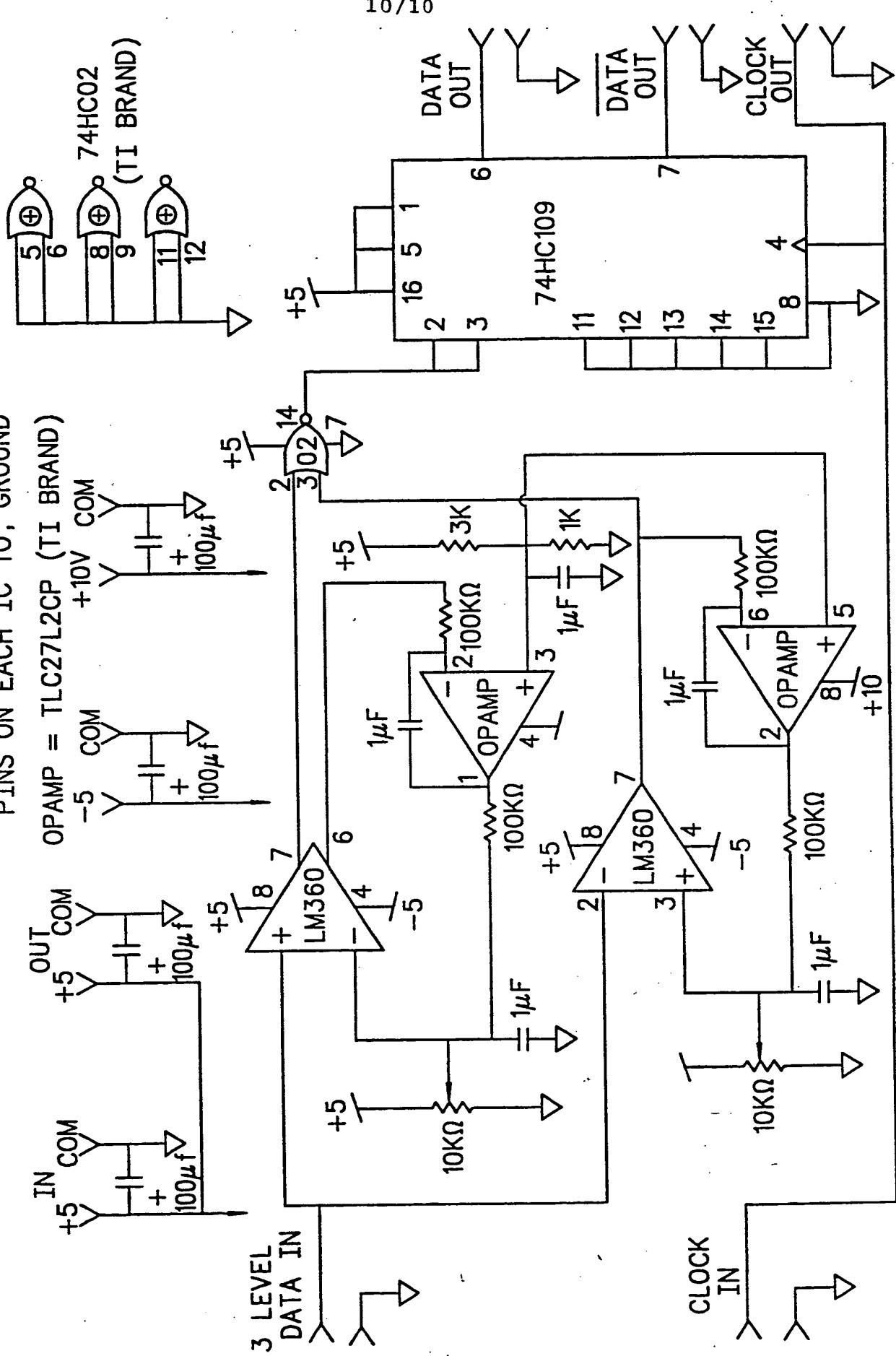


FIG. 8B
PUT 0.1 μ f ACROSS ALL POWER PINS ON EACH IC TO GROUND



INTERNATIONAL SEARCH REPORT

International application No.
PCT/US92/07460

A. CLASSIFICATION OF SUBJECT MATTER

IPC(5) :H04H 1/02
US CL :455/5.1; 358/86

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 455/4.1, 6.1, 6.2, 6.3; 358/85; 455/5.1; 358/86

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

NONE

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	US, A, 4,894,789 (YEE) 16 January 1990 See the figure and columns 5-6.	1, 11 2-10
Y, P	US, A, 5,093,718 (Hoarty) 03 March 1992 See figure 14.	2-10
A	US, A, 4,987,486 (Johnson et al.) 22 January 1991 See the abstract and figure 2.	1-11

Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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